



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TESHIROGI, Kazuo et al.**

Serial No.: **10/708,067 (Div. Of S.N. 10/101,169)**

Filed: **February 6, 2004**

For: **SEMICONDUCTOR SUBSTRATE JIG AND METHOD FOR  
MANUFACTURING A SEMICONDUCTOR DEVICE**

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 3, 2004

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The attention of the Patent and Trademark Office is hereby directed to the document listed on the attached Form PTO-1449. A copy of the document is attached. This document was made of record in parent application Serial No. 10/101,169.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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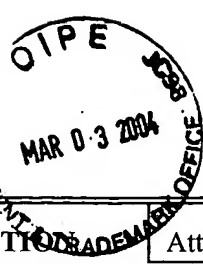
WGK/nrp  
Atty. Docket No. **020334A**  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449 and reference (1)



<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Atty. Docket No. <b>020334A</b>	Serial No. <b>10/708,067</b>
	Applicant(s): <b>TESHIROGI, Kazuo et al.</b>	
	Filing Date: <b>February 6, 2004</b>	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
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_____ AG			
_____ AH			
_____ AI			
_____ AJ			

### OTHER DOCUMENTS

_____	AK	
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Examiner	Date Considered
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